



Attorney Docket No.: SAM-0466  
Application Serial No.: 10/645,018  
Reply to Office Action of: December 1, 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Jae-Hyung Lee  
Serial No.: 10/645,018  
Filing Date: August 21, 2003  
Title: SEMICONDUCTOR MEMORY DEVICE HAVING PARTIALLY  
CONTROLLED DELAY LOCKED LOOP

Examiner: Wells, Kenneth B.  
Group Art Unit: 2816

**CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8**

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1-26-05  
Date

*Vanessa Marakas*  
Vanessa Marakas

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT AFTER FINAL**

Sir:

The following is in response to the Office Action dated December 1, 2004, and is filed within the shortened statutory period of three months. Please amend the application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.